

**Comsats University Islamabad (Lahore Campus)**

**Course Title: Microprocessor and Assembly Language**

**Course Code: CSC321**

**Instructor:**

**Semester/Section: 4th**

**Marks:**

Quiz 2

1. In PUSH instruction, after each execution of the instruction, the stack pointer is
2. incremented by 1
3. decremented by 1
4. incremented by 2
5. **decremented by 2**
6. Which registers are changed by the CMP instruction?
7. Operand
8. destination
9. both
10. **none**
11. LEA AX, var1; What is the output of above instruction? Assume var1 is data word type,
12. **Load complete var1 in AX**
13. Load first value of var1 in AX
14. Syntax Error
15. Logic Error
16. Which of the following is not an arithmetic instruction?
17. INC (increment)
18. CMP (compare)
19. DEC (decrement)
20. **ROL (rotate left)**
21. The instruction, TEST to compare source and destination operands it performs
22. **AND**
23. OR
24. XOR
25. NOT
26. The Stack is accessed using
27. SP register
28. SS register
29. **SP and SS register**
30. None of the mentioned
31. In the RCL instruction, the contents of the destination operand undergo function as
32. **Carry flag is pushed into LSB & MSB is pushed into the carry flag**
33. carry flag is pushed into MSB & LSB is pushed into the carry flag
34. auxiliary flag is pushed into LSB & MSB is pushed into the carry flag
35. parity flag is pushed into MSB & LSB is pushed into the carry flag
36. In MUL or IMUL, CF/OF =1 sets when
37. **the product is too big to fill in 'the lower half of " the destination**
38. the product signed is unchanged
39. the product is too big to fill in 'the lower half of " the source
40. the product signed is changed
41. How many status flags effected by CBW or CWD
42. All status flags
43. OF, CF, ZF
44. ZF, PF, SF
45. **none**

17. what is value of AL after execution of code.

mov AL,8Ah

mov CL,2

shr AL,CL

1. 21h
2. 56h
3. 32h
4. **22h**

18. what is the mathematical express of following code

Mov bl,X

Sub bl,3

Mov al,8

Imul bl

Mov Y,al

1. Y=x+3\*8
2. Y= X-8\*3
3. Y= 8\*x-3
4. **Y= x-3\*8**

26. After execution of DEC AL where AL = 67h which flags effects

1. None
2. PF,ZF,CF
3. **Only PF**
4. AF
5. Divide overflow occur when
6. **Dividend is larger than divisor.**
7. Dividend is smaller than divisor
8. Dividend and divisor both are equal
9. Dividend has negative value and divisor have positive value
10. Which statement match following the code

MOV AX, -1250

CWD

MOV BX,7

IDIV BX

1. **Divide -1250 by 7**
2. Divide 1250 by 7
3. Divide FB1E by 7
4. Divide F1EB by 7
5. 1110 1010 AND 1010 0000
6. **1010 0000**
7. 1000 0000
8. 1010 1010
9. 1011 1100
10. A bit pattern used in logical operations to clear, set, or test. specific bits in an operand is called
11. Rotate
12. Compare
13. **Mask**
14. Not
15. \_\_\_\_\_\_\_ points to the top of the stack.
16. SP::IP
17. **SP:SS**
18. IP:SS
19. SS:IP
20. After byte division, \_\_\_\_\_ has the quotient and \_\_\_\_\_ the remainder
21. AL and BL
22. BL and CH
23. **AL and AH**
24. DL and DH
25. Which logical operation is to convert ASCII digit into number?
26. **And**
27. Not
28. Xor
29. OR
30. SHL Instruction can be used to multiply an operand multiples of \_\_\_\_\_\_\_
31. 4
32. 8
33. **2**
34. 1